
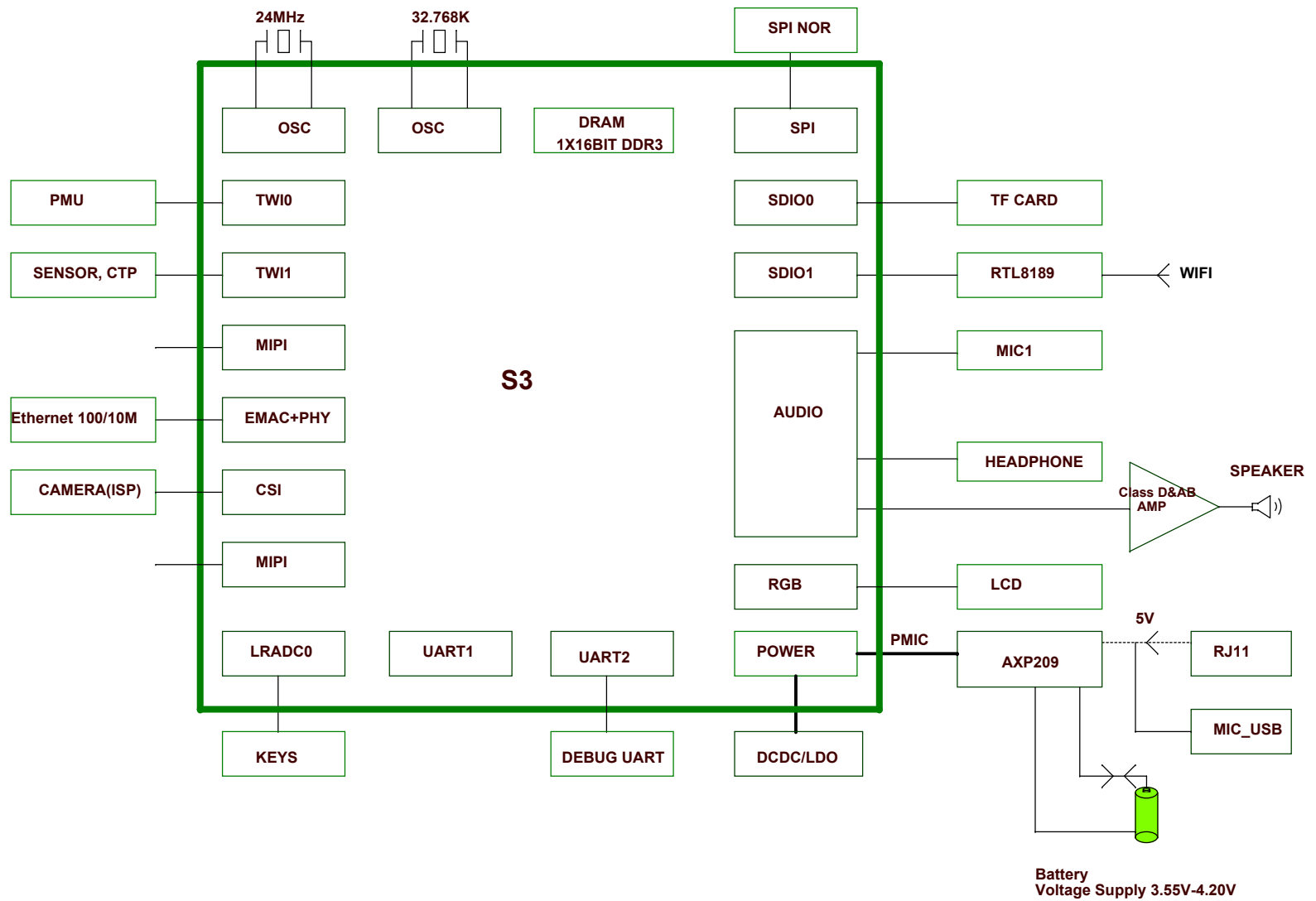


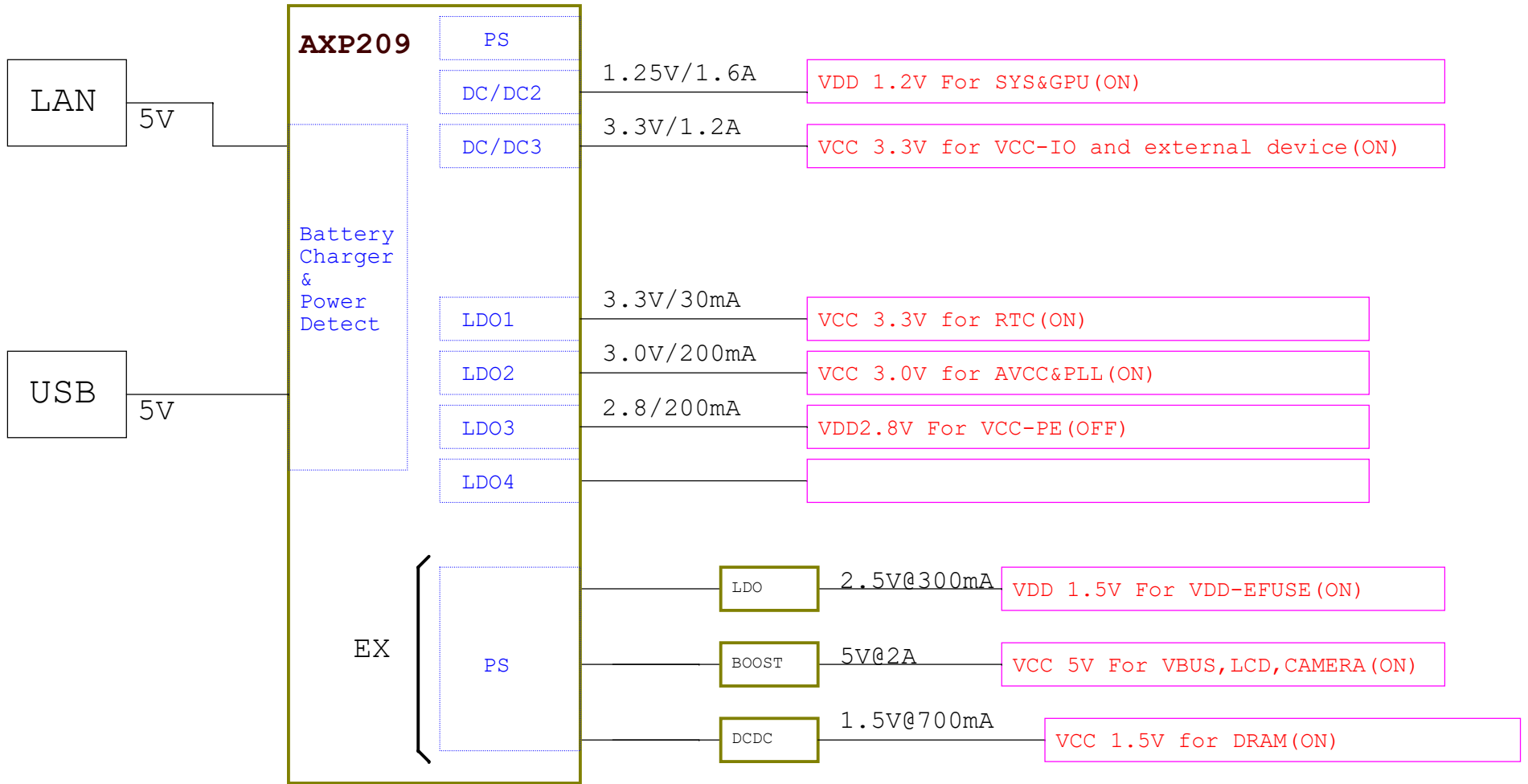
REVISION HISTORY

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GPIO ASSIGNMENT

PIN	Define	CFG	Function
PB0	UART2-TX	2	UART
PB1	UART2-RX	2	
PB2	WIFI-EN	1	WIFI
PB3	WIFI-RST	1	
PB4	PWM0	2	LCD
PB5	PA-SHDN	1	AUDIO
PB6	TWI0-SCK	2	PMU
PB7	TWI0-SDA	2	
PB8	TWI1_SCK	2	DEVICE
PB9	TWI1_SDA	2	
PB10	JTAG-MS	2	JTAG
PB11	JTAG-CK	2	
PB12	JTAG-DO	2	
PB13	JTAG-DI	2	

PIN	Define	CFG	Function
PC0	SPI0_MISO	3	NOR/ NAND
PC1	SPI0_CLK	3	
PC2	SPI0_CS	3	
PC3	SPI0_MOSI	3	
PC4	RECOVERY	0	
PC5	USB-DRVVBUS	1	
PC6	USB-ID	0	
PC7			
PC8			
PC9			
PC10			

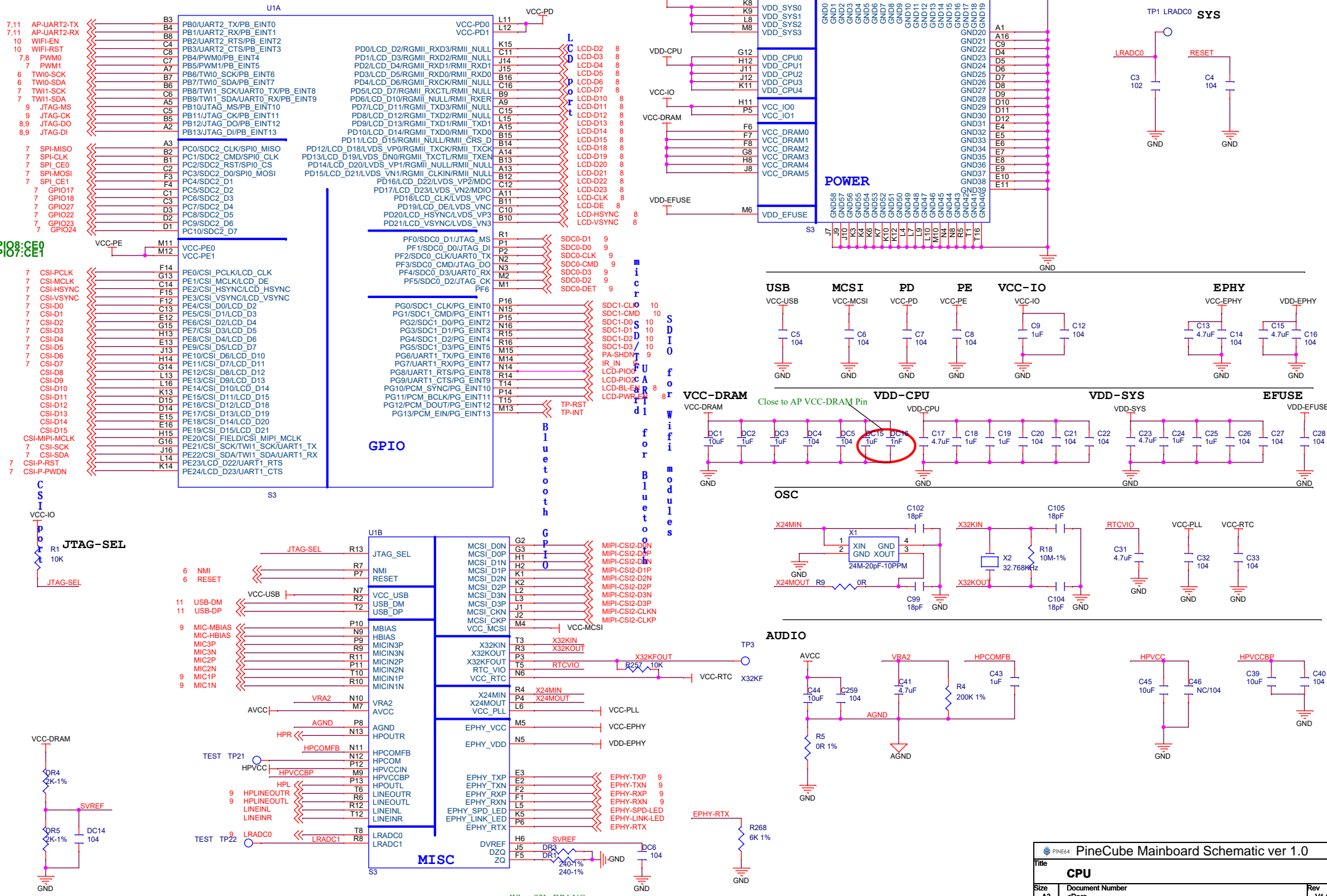
PIN	Define	CFG	Function
PD0	LCD_D2	2	LCD
PD1	LCD_D3	2	
PD2	LCD_D4	2	
PD3	LCD_D5	2	
PD4	LCD_D6	2	
PD5	LCD_D7	2	
PD6	LCD_D10	2	
PD7	LCD_D11	2	
PD8	LCD_D12	2	
PD9	LCD_D13	2	
PD10	LCD_D14	2	
PD11	LCD_D15	2	
PD12	LCD_D18	2	
PD13	LCD_D19	2	
PD14	LCD_D20	2	
PD15	LCD_D21	2	
PD16	LCD_D22	2	
PD17	LCD_D23	2	
PD18	LCD_CLK	2	
PD19	LCD_DE	2	
PD20	LCD_HSYNC	2	
PD21	LCD_VSYNC	2	

PIN	Define	CFG	Function
PE0	CSI_PCLK	2	CSI
PE1	CSI_MCLK	2	
PE2	CSI_HSYNC	2	
PE3	CSI_VSYNC	2	
PE4	CSI_D0	2	
PE5	CSI_D1	0	
PE6	CSI_D2	2	
PE7	CSI_D3	2	
PE8	CSI_D4	2	
PE9	CSI_D5	2	
PE10	CSI_D6	2	
PE11	CSI_D7	2	
PE12	CSI_D8	2	
PE13	CSI_D9	2	
PE14	CSI_D10	2	
PE15	CSI_D11	2	
PE16	CSI-PWREN	1	
PE17	CSI-FLASH-IO0	1	
PE18	CSI-AF-IO	1	
PE19	CSI-FLASH-IO1	1	
PE20	CSI-MIPI-MCLK	1	
PE21	CSI-SCK	2	
PE22	CSI-SDA	2	
PE23	CSI-P-RST	1	
PE24	CSI-P-PWDN	1	

PIN	Define	CFG	Function
PF0	JTAG_MS	3	CARD0
PF1	JTAG_DI	3	
PF2	UART0_TX	3	
PF3	JTAG_DO	3	
PF4	UART0_RX	3	
PF5	JTAG_CK	3	
PF6	SDC0-DET	0	

PIN	Define	CFG	Function
PG0	SDC1-CLK	2	WIFI
PG1	SDC1-CMD	2	
PG2	SDC1-D0	2	
PG3	SDC1-D1	2	
PG4	SDC1-D2	2	
PG5	SDC1-D3	2	CTP
PG6	CTP-RST	1	
PG7	CTP-INT	0	LCD
PG8	LCD-PIO0	1	
PG9	LCD-PIO2	1	
PG10	LCD-PIO1	1	
PG11	LCD-PWR-EN	1	
PG12	MIPI-RESET	1	
PG13	MIPI-PWDN	1	

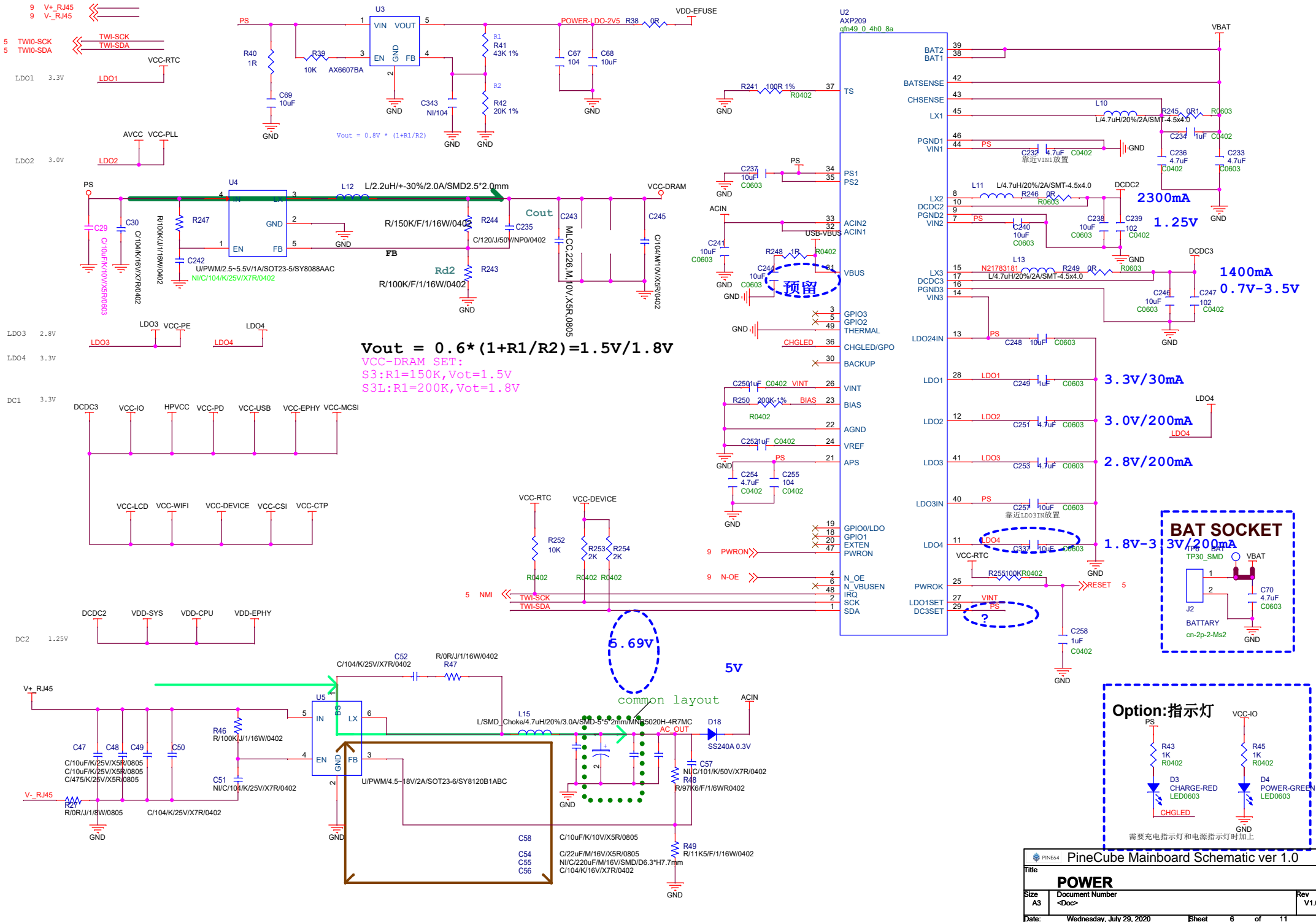
CPU



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CPU			
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When S3L_DR1 NC

POWER



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POWER		
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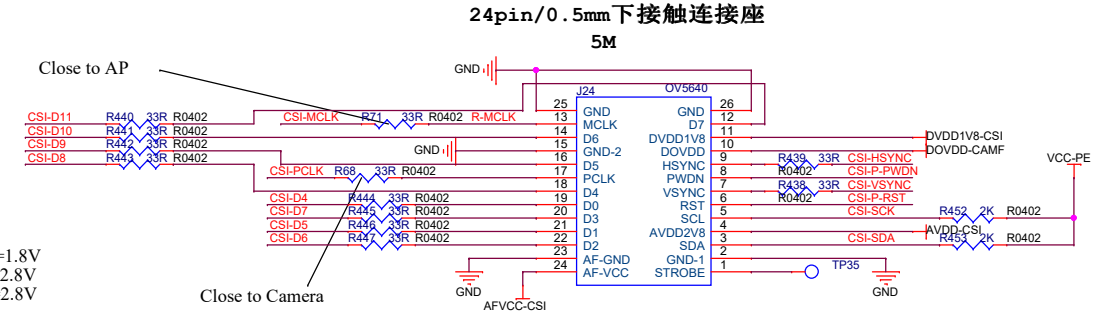
- 5.7 CSI-MIPI-MCLK <<<
- 5.7 CSI-SCK <<<
- 5.7 CSI-SDA <<<
- 5.7 CSI-D14 <<<
- 5.7 CSI-D15 <<<

Differential pairs
Z0= 100 ohm



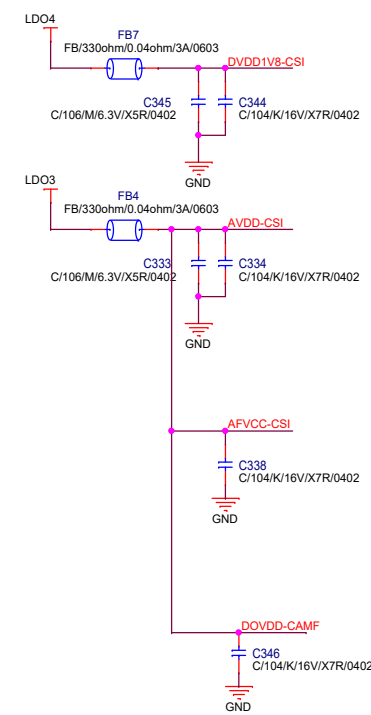
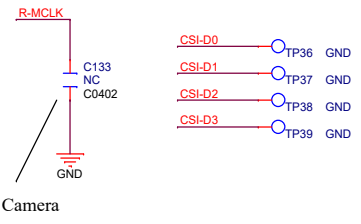
- 5 CSI-PCLK <<<
- 5 CSI-MCLK <<<
- 5 CSI-HSYNC <<<
- 5 CSI-VSYNC <<<
- 5 CSI-D0 <<<
- 5 CSI-D1 <<<
- 5 CSI-D2 <<<
- 5 CSI-D3 <<<
- 5 CSI-D4 <<<
- 5 CSI-D5 <<<
- 5 CSI-D6 <<<
- 5 CSI-D7 <<<
- 5 CSI-D8 <<<
- 5 CSI-D9 <<<
- 5 CSI-D10 <<<
- 5 CSI-D11 <<<
- 5 CSI-D12 <<<
- 5 CSI-D13 <<<
- 5.7 CSI-D14 <<<
- 5.7 CSI-D15 <<<
- 5.7 CSI-MIPI-MCLK <<<
- 5.7 CSI-SCK <<<
- 5.7 CSI-SDA <<<
- 5 CSI-P-RST <<<
- 5 CSI-P-PWDN <<<

CSI Camera



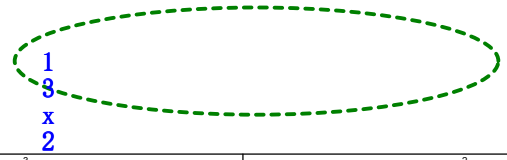
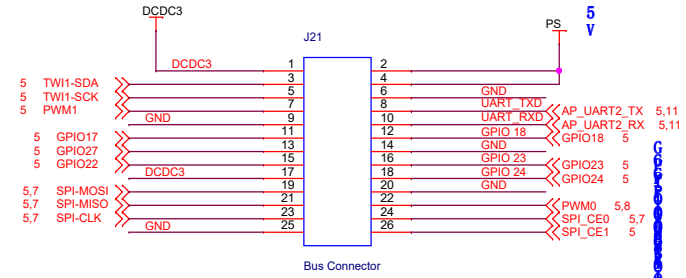
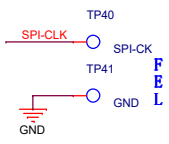
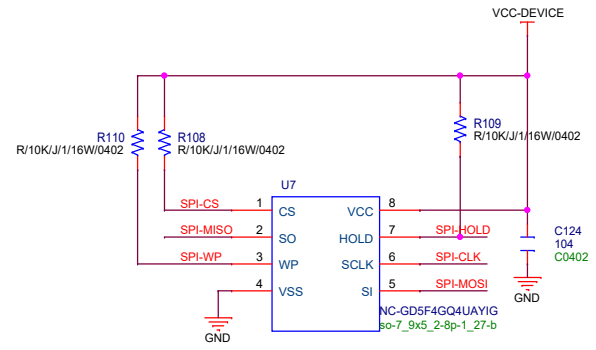
DVDD1V8-CSI = 1.8V
DOVDD-CSI = 2.8V
AVDD-CSI = 2.8V

DVDD1V8-CSI = 1.8V
DOVDD-CSI = 2.8V
AVDD-CSI = 2.8V
AFVCC-CSI = 2.8V



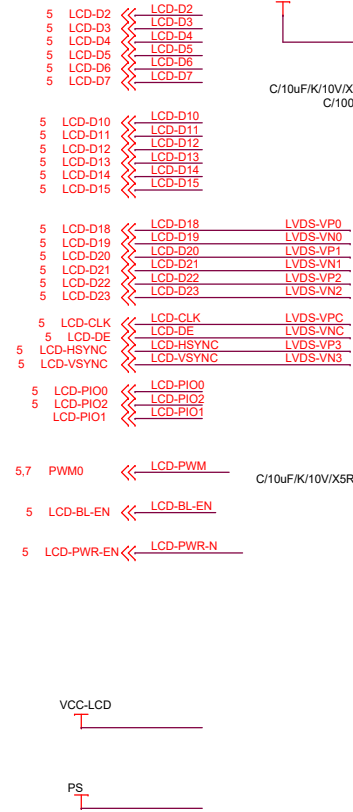
NOR NAND in 1

- 5.7 SPI_CE0 <<< SPI_CS
- 5.7 SPI_MISO <<< SPI_MISO
- 5.7 SPI_CLK <<< SPI_CLK
- 5.7 SPI_MOSI <<< SPI_MOSI

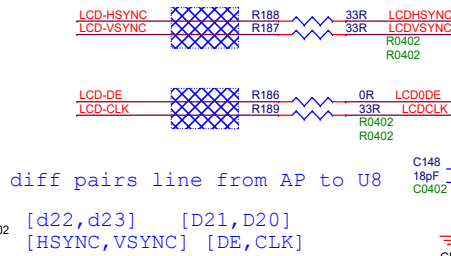
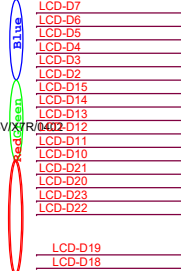
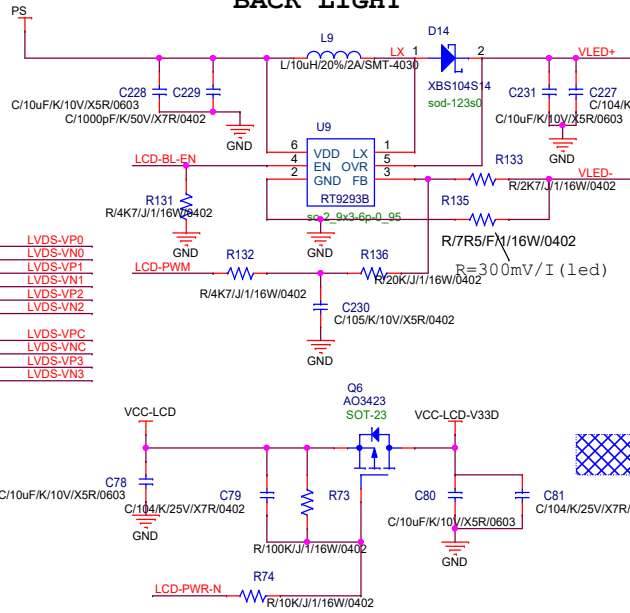


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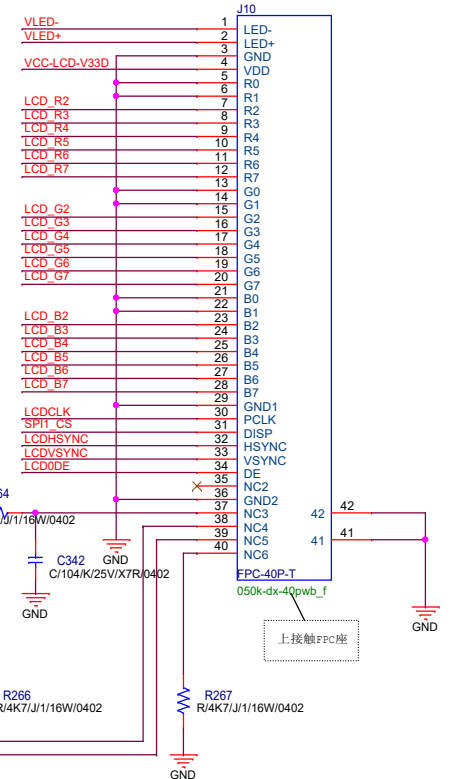
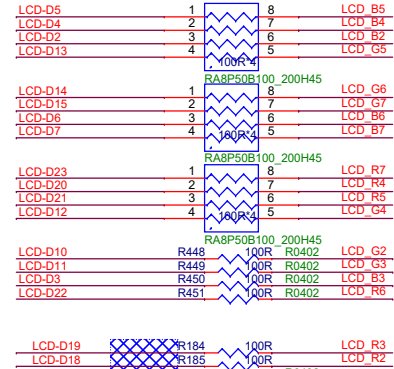
RGB LCD

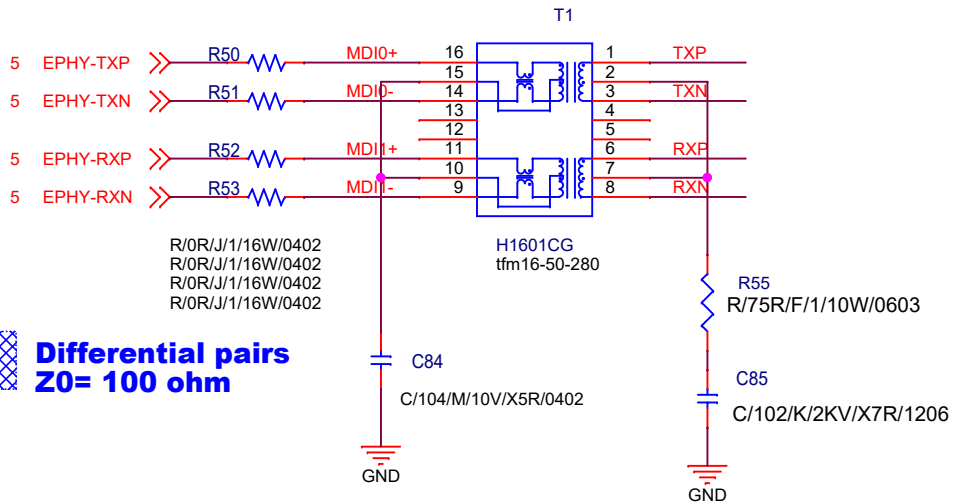


BACK LIGHT

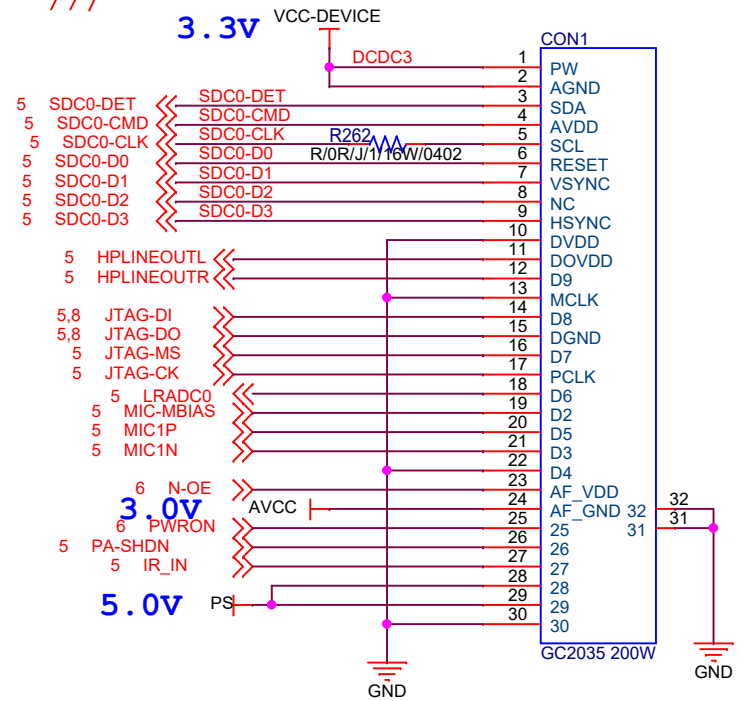
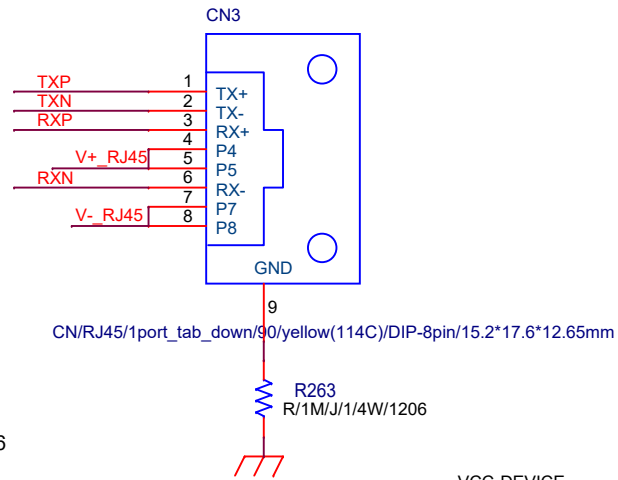
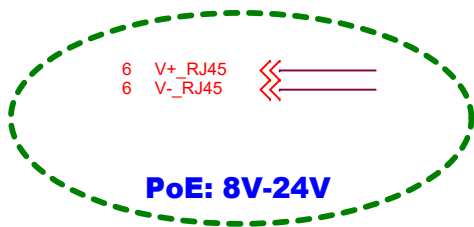


diff pairs line from AP to U8
[d22,d23] [D21,D20]
[HSYNC,VSYNC] [DE,CLK]



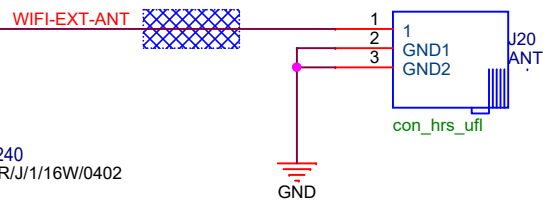
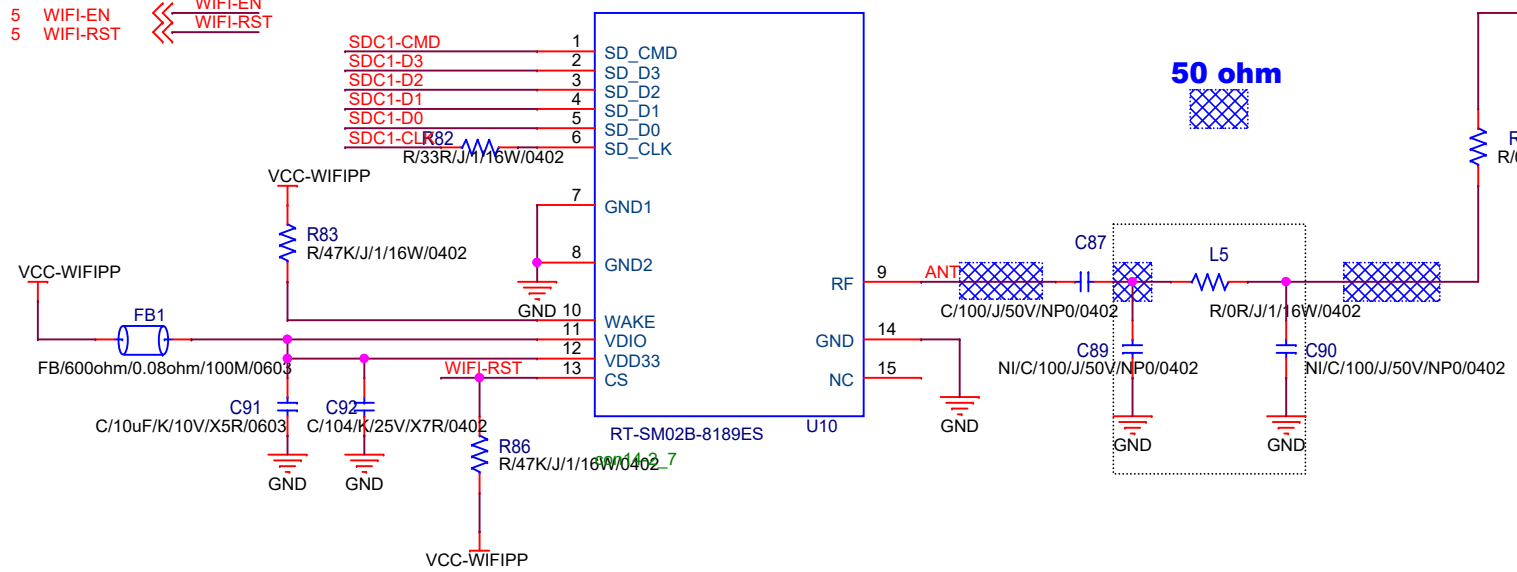


Differential pairs
Z0= 100 ohm

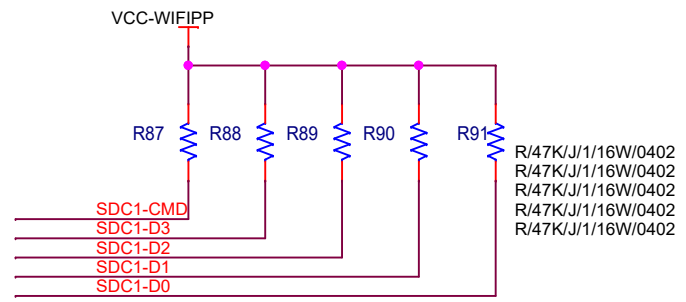
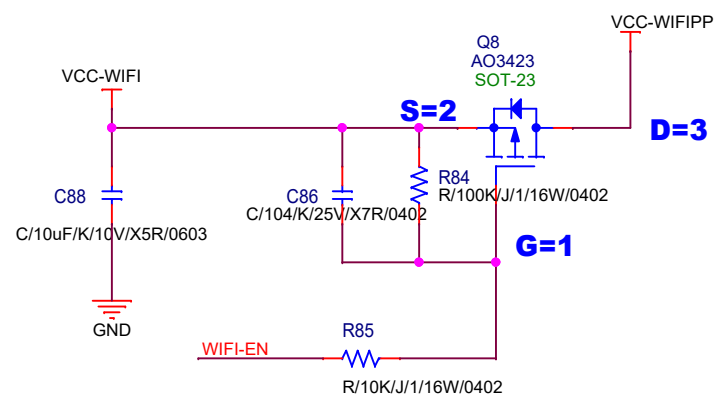


- 5 SDC1-CMD
- 5 SDC1-CLK
- 5 SDC1-D3
- 5 SDC1-D2
- 5 SDC1-D1
- 5 SDC1-D0

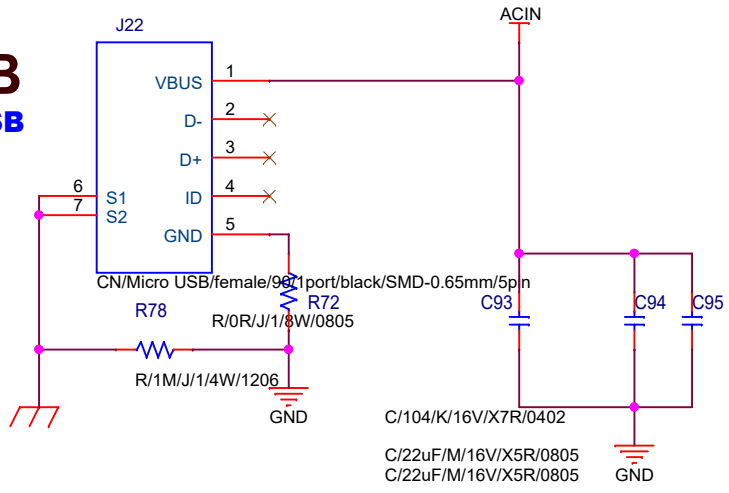
- 5 WIFI-EN
- 5 WIFI-RST



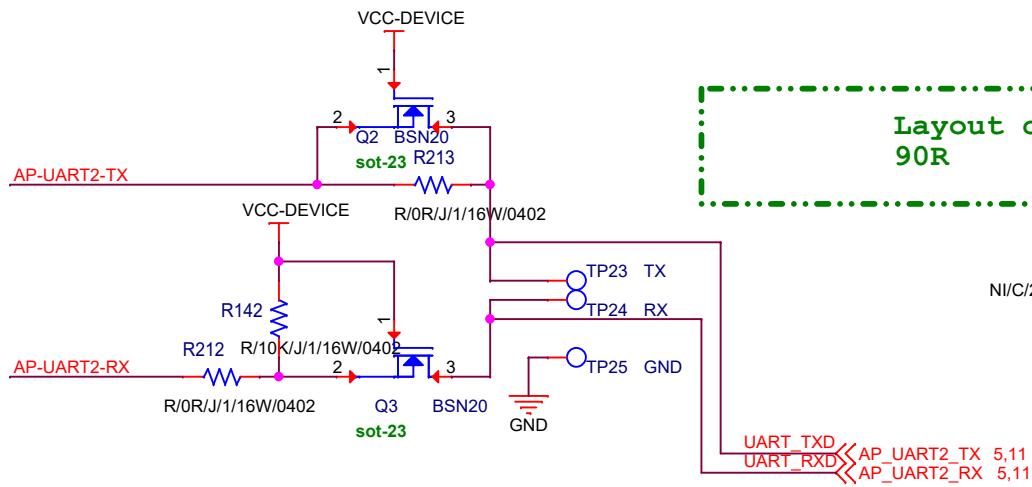
adjust according to actual case



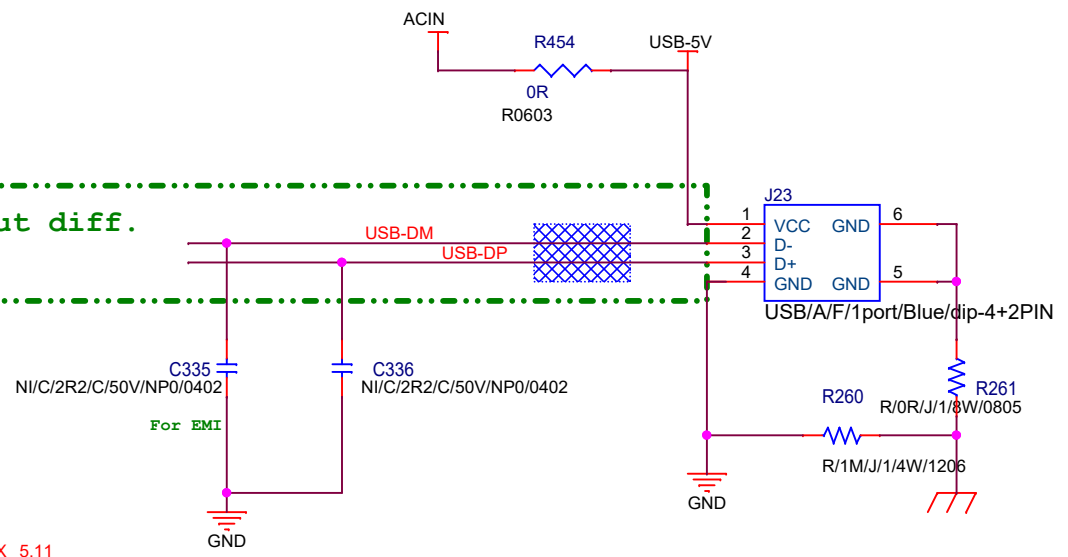
USB MIRCO USB



UART

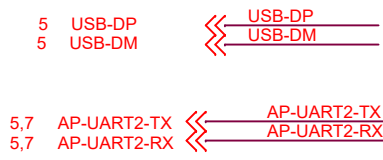


Layout diff.
90R



Differential pairs
Z0= 90 ohm

USB TYPE A



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